

# PATENT ABSTRACTS OF JAPAN

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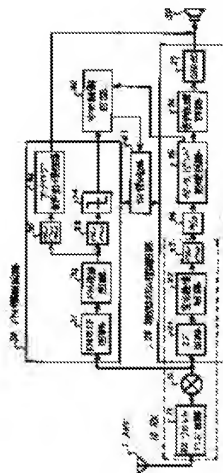
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## (54) POWER CONSERVATION DEVICE

(57)Abstract:

**PROBLEM TO BE SOLVED:** To reduce the power consumption in the case of demodulating an FM signal through the use of a demodulation circuit with high power consumption where the signal is processed by using a digital signal processor(DSP) after orthogonal detection.

**SOLUTION:** The device has a demodulation circuit 20 that demodulates an FM signal and a signal of an other modulation system and requires high power consumption and a demodulation circuit 30 that demodulates an FM signal only and requires only small power consumption, and in the case of receiving the FM modulation signal,



the operation of the demodulation circuit 30 with small power consumption is stopped and the demodulation circuit 20 demodulates the signal.